



3A Fast-Response LDO Regulator

General Description

The MIC29302A is a high-current, low-cost, low-dropout voltage regulator that uses Micrel's proprietary Super β eta PNP[®] process with a PNP pass element. The 3A LDO regulator features 450mV (full load) dropout voltage and very low ground current. Designed for high-current loads, these devices also find applications in lower current, low-dropout critical systems, where their dropout voltages and ground current values are important attributes.

Along with a total accuracy of $\pm 2\%$ (over temperature, line, and load regulation) the regulator features very fast transient recovery from input voltage surges and output load current changes.

The MIC29302A has an adjustable output that can be set by two external resistors to a voltage between 1.24V and 15V. In addition, the device is fully protected against overcurrent faults, reversed input polarity, reversed lead insertion, and overtemperature operation. A TTL logic enable (EN) pin is available in the MIC29302A to shutdown the regulator. When not used, the device can be set to continuous operation by connecting EN to the input (IN). The MIC29302A is available in the standard and 5-pin

TO-263 and TO-252 packages with an operating junction temperature range of -40° C to $+125^{\circ}$ C.

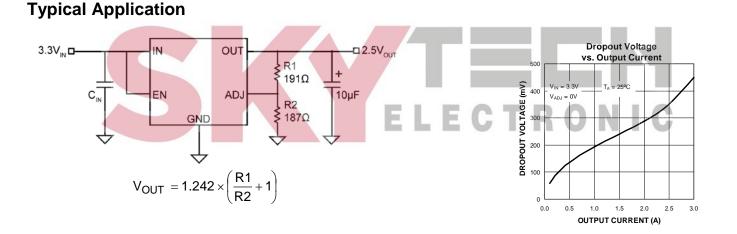
Datasheets and support documentation can be found on Micrel's website at: <u>www.micrel.com</u>.

Features

- High-current capability
 - 3A over full temperature range
- Low-dropout voltage of 450mV at full load
- Low ground current
- Accurate 1% guaranteed tolerance
- Extremely fast transient response
- Zero-current shutdown mode
- Error flag signals output out-of-regulation
- Adjustable output voltage
- Packages: TO-263-5L and TO-252-5L

Applications

- Processor peripheral and I/O supplies
- High-efficiency "green" computer systems
- Automotive electronics
- High-efficiency linear lower supplies
- Battery-powered equipment
- PC add-in cards
- High-efficiency post-regulator for switching supply



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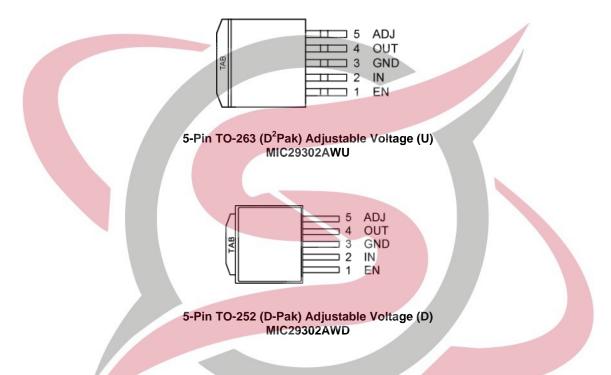
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Ordering Information

Part Number	Voltage	Junction Temperature Range	Package
MIC29302AWU	Adjustable	−40°C to +125°C	5-Pin TO-263
MIC29302AWD	Adjustable	-40°C to +125°C	5-Pin TO-252

Pin Configuration



Pin Description

Pin Number TO-263	Pin Number TO-252	Pin Name	Pin Function	
1	1	EN	Enable (Input): Active-high CMOS compatible control input. Do not float.	
2	2	IN	INPUT: Unregulated input, +3V to +16V maximum	
3, TAB	3, TAB	GND	GND GND: TAB is also connected internally to the IC's ground on both packages.	
4	4	OUT	OUTP <mark>UT: The</mark> regulator output voltage	
5	5	ADJ	Feedback Voltage: 1.24V feedback from external resistor divider.	
		10		

Absolute Maximum Ratings⁽¹⁾

Input Supply Voltage (V _{IN}) ⁽¹⁾	20V to +20V
Enable Input Voltage (V _{EN})	–0.3V to V _{IN}
Lead Temperature (soldering, 5 sec.)	
Power Dissipation	Internally Limited
Storage Temperature Range ESD Rating all pins ⁽³⁾	65°C to +150°C

Operating Ratings⁽²⁾

Operating Junction Temperature	40°C to +125°C
Operating Input Voltage	3V to 16V
Package Thermal Resistance	
TO-263 (θ _{JC})	3°C/W
TO-252 (θ _{JC})	3°C/W
ΤΟ-252 (θ _{JA})	35°C/W
ΤΟ-263 (θ _{JA})	28°C/W

Electrical Characteristics⁽⁴⁾

 V_{IN} = 4.2V; I_{OUT} = 100mA; T_A = 25°C, **bold** values indicate -40°C≤ T_J ≤ +125°C, unless noted.

Parameter	Condition	Min.	Тур.	Max.	Units	
Output Voltage						
Output Voltage Accuracy ⁽⁵⁾	$100\text{mA} \le I_{\text{OUT}} \le 3A$, $(V_{\text{OUT}} + 1V) \le V_{\text{IN}} \le 16V$	-2		2	%	
Line Regulation	$I_{OUT} = 100mA, (V_{OUT} + 1V) \le V_{IN} \le 16V$		0.1	0.5	%	
Load Regulation	$V_{IN} = V_{OUT} + 1V$, 100mA $\leq I_{OUT} \leq 3A$		0.2	1	%	
	I _{OUT} = 100mA		80	200	-	
	$I_{OUT} = 750 \text{mA}$		220			
	I _{OUT} = 1.5A		275		mV	
Dropout Voltage ⁽⁶⁾	I _{OUT} = 2.0A		375	630		
	I _{OUT} = 2.5A		430	720		
	I _{OUT} = 3A		450	800		
Ground Current						
	$I_{OUT} = 750$ mA, $V_{IN} = V_{OUT} + 1V$		5	20	mA	
Ground Current	lout = 1.5A		15			
	I _{OUT} = 3A		60	150		
I _{GRNDDO} Ground Pin Current @ Dropout	V_{IN} = 0.5V less than specified $V_{\text{OUT}} \times I_{\text{OUT}}$ = 10mA		2			
Current Limit	$V_{OUT} = 0V^{(7)}$	3	4		A	
e _n , Output Noise Voltage (10Hz to 100kHz	C _L = 10µF 40		400		μV _{RMS}	
I _L = 100mA	С _L = 33µF		260			
Ground Pin Current in Shutdown	Input Voltage V _{IN} = 16V		32		μA	

Notes:

1. Exceeding the absolute maximum rating may damage the device.

2. The device is not guaranteed to function outside its operating rating.

3. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5kΩ in series with 100pF.

4. Specification for packaged product only

5. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature change.

- 6. Dropout voltage is defined as the input-to-output differential when output voltage drops to 99% of its normal value with V_{OUT} + 1V applied to V_{IN}.
- 7. VIN =VOUT (nominal) + 1V. For example, use VIN = 4.3V for a 3.3V regulator or use 6V for a 5V regulator. Employ pulse testing procedure for current limit.

Electrical Characteristics⁽⁴⁾ (Continued)

 $V_{\text{IN}} = 4.2 \text{V}; \text{ } I_{\text{OUT}} = 10 \text{mA}; \text{ } T_{\text{A}} = 25^{\circ}\text{C}, \text{ bold } \text{values indicate } -40^{\circ}\text{C} \leq T_{\text{J}} \leq +125^{\circ}\text{C}, \text{ unless noted}.$

Reference
Reference

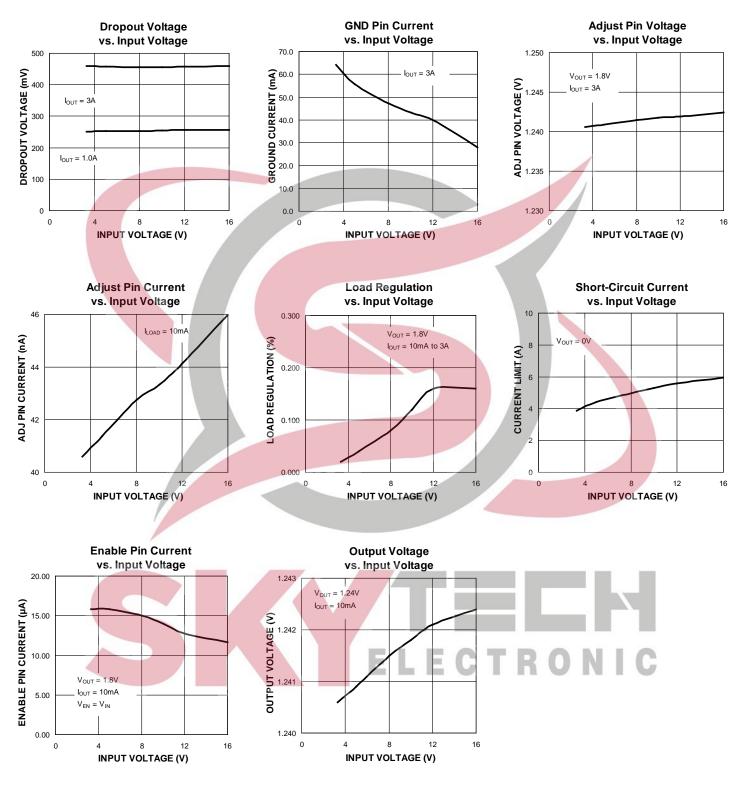
Reference Voltage	Note 8	1.215	1.267	V
Adjust Pin Bias Current			40	
			120	nA
ENABLE Input	· · ·	····		•
Innut Logio Valtago	Low (OFF)		0.8	V
Input Logic Voltage	High (ON)	2.4	1	
	$V_{EN} = 4.2V$		15 30	μA
Fachle Dis last Ourset			75	
Enable Pin Input Current	$V_{EN} = 0.8V$		2	
			4	
Regulator Output Current	Note 10		10	
in Šhutdown			20	μA

Notes:

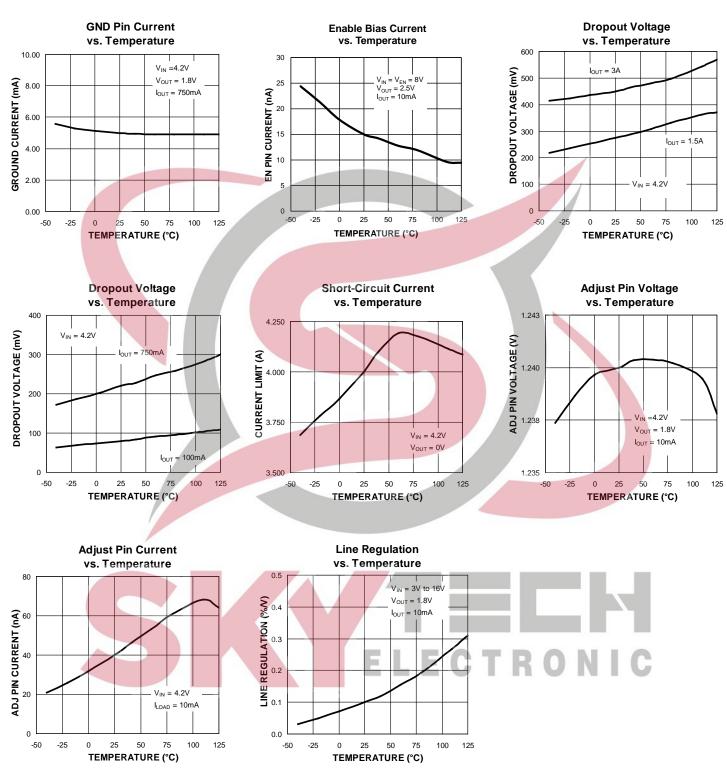
- $8. \quad V_{REF} \leq V_{OUT} \leq V_{IN} \textbf{ -1, } \textbf{ 3V} \leq V_{OUT} \leq \textbf{ 16V, } \textbf{ 10mA} \leq \textbf{I}_L \leq \textbf{I}_{FL}, \textbf{ T}_J \leq \textbf{T}_{Jmax} \textbf{.}$
- 9. Thermal regulation is defined as the change in the output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 250mA load pulse at V_{IN} = 16V (a 4W pulse) for T = 10ms.
- 10. $V_{EN} \leq 0.8V, V_{IN} \leq 16V$ and $V_{OUT} = 0V$.



Typical Characteristics



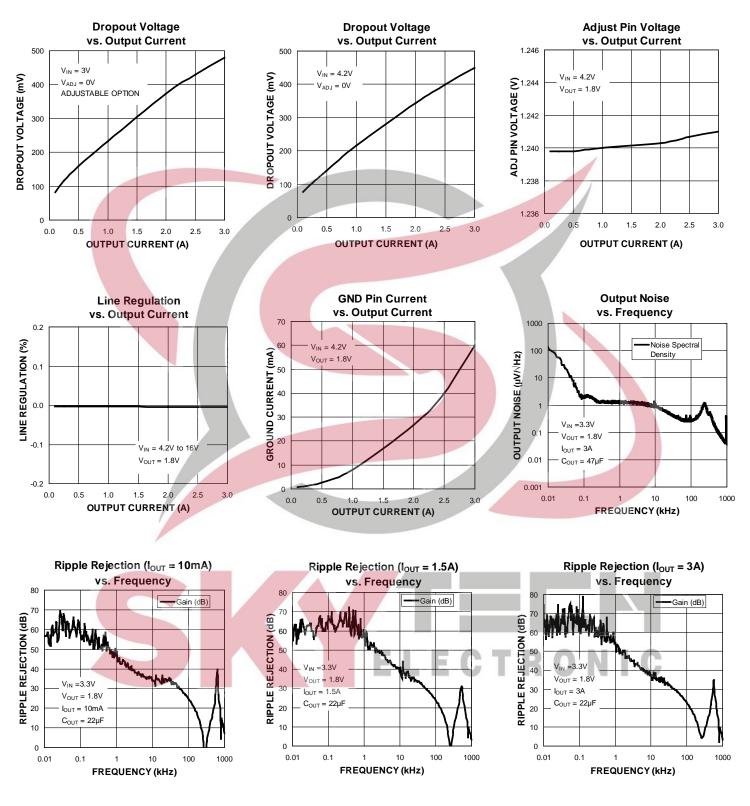
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Typical Characteristics (Continued)

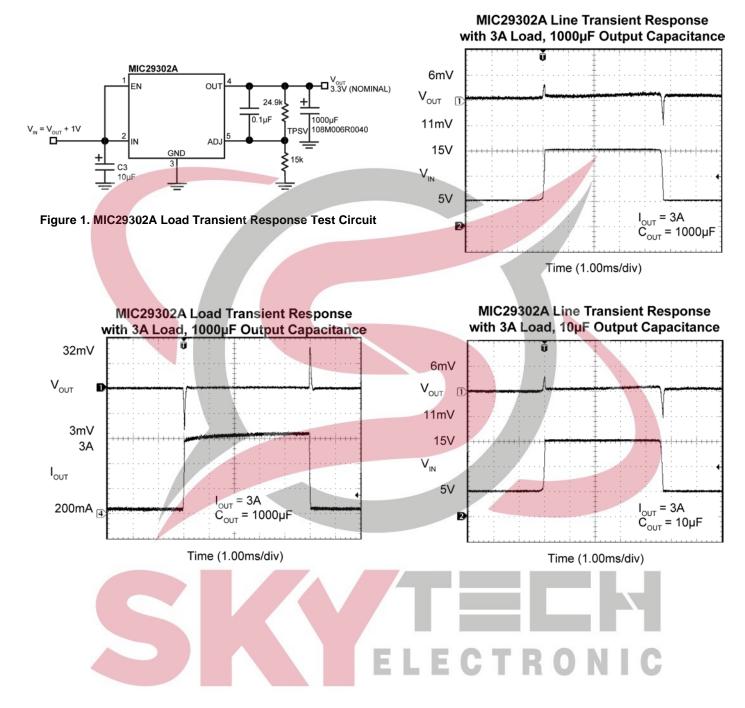
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Typical Characteristics (Continued)

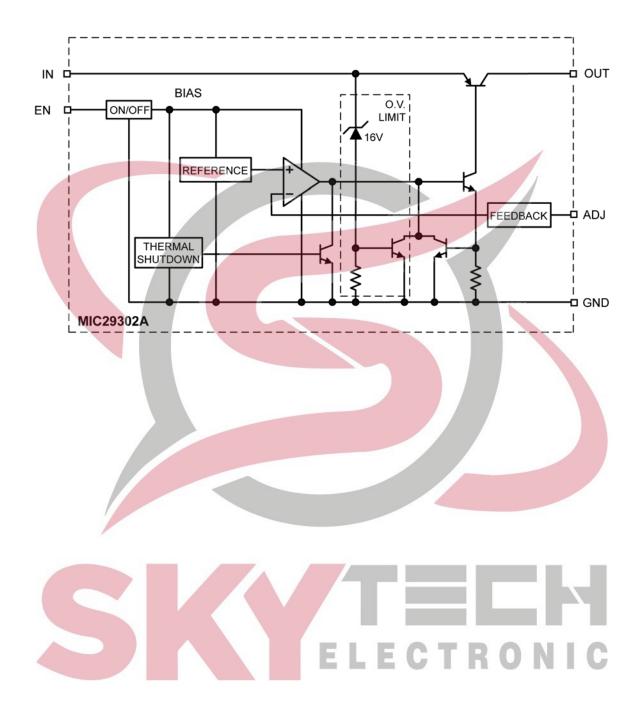


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Functional Characteristics



Functional Diagram



Application Information

The MIC29302A is a high-performance, low-dropout voltage regulator suitable for all moderate to high-current voltage regulation applications. Its 450mV typical dropout voltage at full load makes it especially valuable in battery-powered systems and as high efficiency noise filters in post-regulator applications. Unlike older NPN-pass transistor designs, where the minimum dropout voltage is limited by the base-emitter voltage drop and collector-emitter saturation voltage, dropout performance of the PNP output is limited merely by the low V_{CE} saturation voltage.

A trade-off for the low-dropout voltage is a varying base driver requirement. But Micrel's Super ßeta PNP[®] process reduces this drive requirement to merely 1% of the load current.

The MIC29302A regulator is fully protected from damage due to fault conditions. Current limiting is linear; output current under overload conditions is constant. Thermal shutdown disables the device when the die temperature exceeds the +125°C maximum safe operating temperature. The output structure of the regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow. The MIC29302A offers a logic-level ON/OFF control. When disabled, the device draws nearly zero current.

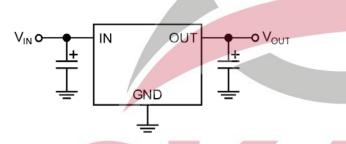


Figure 2. Linear Regulators Require Only Two Capacitors for Operation

Thermal Design

Linear regulators are simple to use. The most complicated set of design parameters to consider are thermal characteristics. Thermal design requires the following application-specific parameters:

- Maximum ambient temperature, T_A
- Output Current, I_{OUT}
- Output Voltage, VOUT
- Input Voltage, V_{IN}

First, we calculate the power dissipation of the regulator from these numbers and the device parameters from this datasheet:

$$\mathsf{P}_{\mathsf{D}} = \mathsf{I}_{\mathsf{OUT}} \left(1.02 \mathsf{V}_{\mathsf{IN}} - \mathsf{V}_{\mathsf{OUT}} \right)$$

Where the ground current is approximated by 2% of I_{OUT} . Then the heatsink thermal resistance is determined with this formula:

$$\theta_{SA} = \frac{T_{JMAX} - T_A}{P_D} - (\theta_{JC} + \theta_{CS})$$

where:

 $T_{\text{JMAX}} \leq 125^{\circ}\text{C}$ and θ_{CS} is between 0°C/W and 2°C/W.

The heatsink may be significantly reduced in applications where the minimum input voltage is known and is large compared to the dropout voltage. A series input resistor can be used to drop excessive voltage and distribute the heat between this resistor and the regulator. The low-dropout properties of Micrel Super β eta PNP[®] regulators allow very significant reductions in regulator power dissipation and the associated heatsink without compromising performance. When this technique is employed, a capacitor of at least 0.1µF is needed directly between the input and regulator ground.

Please refer to *Application Note 9* and *Application Hint* 17 on Micrel's website (<u>www.micrel.com</u>) for further details and examples on thermal design and heatsink specification.

With no heatsink in the application, calculate the junction temperature to determine the maximum power dissipation that will be allowed before exceeding the maximum junction temperature of the MIC29302A. The maximum power allowed can be calculated using the thermal resistance (θ_{JA}) of the D-Pak (TO252) adhering to the following criteria for the PCB design: 2oz. copper and 100mm² copper area for the MIC29302A.

For example, given an expected maximum ambient temperature (T_A) of 75°C with $V_{IN} = 3.3V$, $V_{OUT} = 2.5V$, and $I_{OUT} = 3A$, first calculate the expected P_D using:

$$P_{D} = (3.3V - 2.5V) \times 3A - (3.3V) \times (0.016A) = 2.3472W$$

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Next, calcualte the junction temperature for the expected power dissipation:

$$T_J = (\theta_{JA} \times P_D) + T_A = (35^{\circ}C/W \times 2.3472W)$$

+ 75^{\circ}C = 157.15^{\circ}C

Now determine the maximum power dissipation allowed that would not exceed the IC's maximum junction temperature (125°C) without the use of a heatsink by:

$$P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$$

= (125°C - 75°C) / (35°C/W)
= 1.428W

Capacitor Requirements

For stability and minimum output noise, a capacitor on the regulator output is necessary. The value of this capacitor is dependent upon the output current; lower currents allow smaller capacitors. The MIC29302A is stable with a 10μ F capacitor at full load.

This capacitor need not be an expensive low-ESR type; aluminum electrolytics are adequate. In fact, extremely low-ESR capacitors may contribute to instability. Tantalum capacitors are recommended for systems where fast load transient response is important.

When the regulator is powered from a source with high AC impedance, a 0.1μ F capacitor connected between input and GND is recommended.

Transient Response and 5V to 3.3V Conversion

The MIC29302A has excellent response to variations in input voltage and load current. By virtue of its low dropout voltage, the device does not saturate into dropout as readily as similar NPN-based designs. A 3.3V output Micrel LDO will maintain full speed and performance with an input supply as low as 4.2V, and will still provide some regulation with supplies down to 3.8V, unlike NPN devices that require 5.1V or more for good performance and become nothing more than a resistor under 4.6V of input. Micrel's PNP regulators provide superior performance in "5V to 3.3V" conversion applications than NPN regulators, especially when all tolerances are considered.

Minimum Load Current

The MIC29302A regulator operates within a specified load range. If the output current is too small, leakage currents dominate and the output voltage rises.

A minimum load current of 10mA is necessary for proper regulation and to swamp any expected leakage current across the operating temperature range. For best performance the total resistance (R1+R2) should be small enough to pass the minimum regulator load current of 10mA.

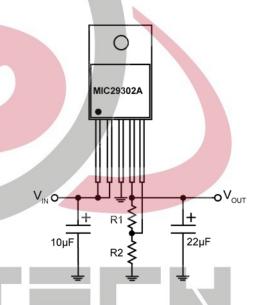
Adjustable Regulator Design

The output voltage can be programmed anywhere between 1.25V and the 15V. Two resistors are used. The resistor values are calculated by:

$$\mathbf{R}_1 = \mathbf{R}_2 \times \left(\frac{\mathbf{V}_{OUT}}{1.240} - 1\right)$$

where V_{OUT} is the desired output voltage.

Figure 3 shows component definition. Applications with widely varying load currents may scale the resistors to draw the minimum load current required for proper operation (see "Minimum Load Current" section).

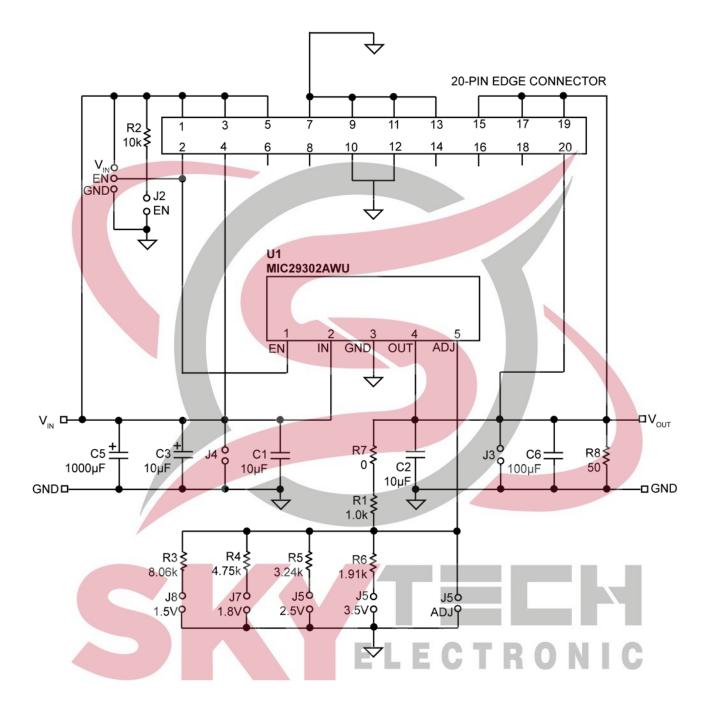




Enable Input

MIC29302A features an enable (EN) input that allows ON/OFF control of the device. Special design allows "zero" current drain when the device is disabled—only microamperes of leakage current flows. The EN input has TTL/CMOS compatible thresholds for simple interfacing with logic, or may be directly tied to V_{IN} . Enabling the regulator requires approximately 20µA of current into the EN pin.

Evaluation Board Schematic



Bill of Materials

ltem	Part Number	Manufacturer	Description	Qty.
C1	C2012X5R1E106K	TDK ⁽¹¹⁾	10µF, 25V, Ceramic Capacitor, X5R, 0805	
C2,C3	B45196H4106K309	Kemet ⁽¹²⁾	40. E 201/ Tantalum Canaditan 2242	2
	TR3C106K020C0450	Vishay ⁽¹³⁾	10µF, 20V, Tantalum Capacitor, 2312	
C5	EEU-FM1E102	Panasonic ⁽¹⁴⁾	1000µF, 25V, Elect Capacitor, through hole, 10X22-case	1
C6	T495D107K016ATE125	Kemet	100µF, 20V, Tantalum Capacitor, 2917	
	TR3D107K016C0125	Vishay		
R1	CRCW06031K00FKTA	Vishay	1k, Resistor, 1%, 0603	1
R2	Open (CRCW06031002FRT1)	Vishay	10k, Resistor, 1%, 0603	1
R3	CRCW06038061FRT1	Vishay	8.06k, Resistor, 1%, 0603	1
R4	CRCW06034751FRT1	Vishay	4.75k, Resistor, 1%, 0603	1
R5	CRCW06033241FRT1	Vishay	3.24k, Resistor, 1%, 0603	1
R6	CRCW06031911RFRT1	Vishay	1.91k, Resistor, 1%, 0603	1
R7	CRCW06030000FKTA	Vishay	0Ω, Resistor, 1%, 0603	1
R8	CRCW060350R0FRT1	Vishay	50Ω, Resistor, 1%, 0603	1
U1	MIC29302AWU	Micrel ⁽¹⁵⁾	3A Fast-Response LDO Regulator	1

Notes:

11. TDK: www.tdk.com.

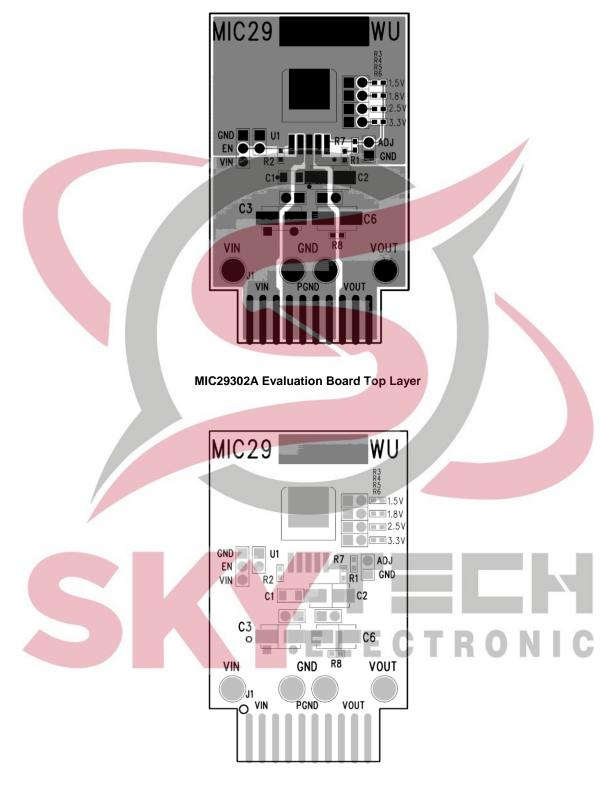
12. Kemet: <u>www.kemet.com</u>.

13. Vishay: <u>www.vishay.com</u>.

- 14. Panasonic.: www.panasonic.com.
- 15. Micrel, Inc.: <u>www.micrel.com</u>.

SKYTECH

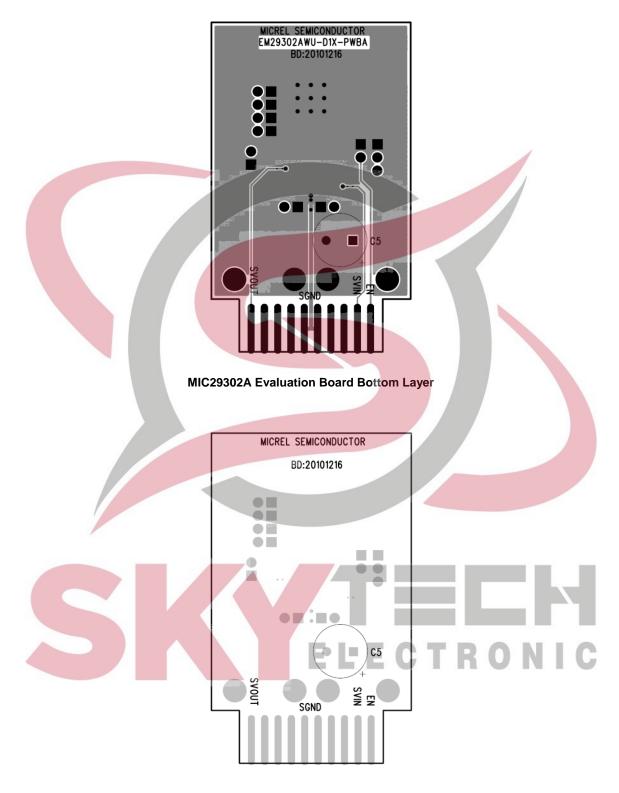
PCB Layout Recommendations



MIC29302A Evaluation Board Top Silk

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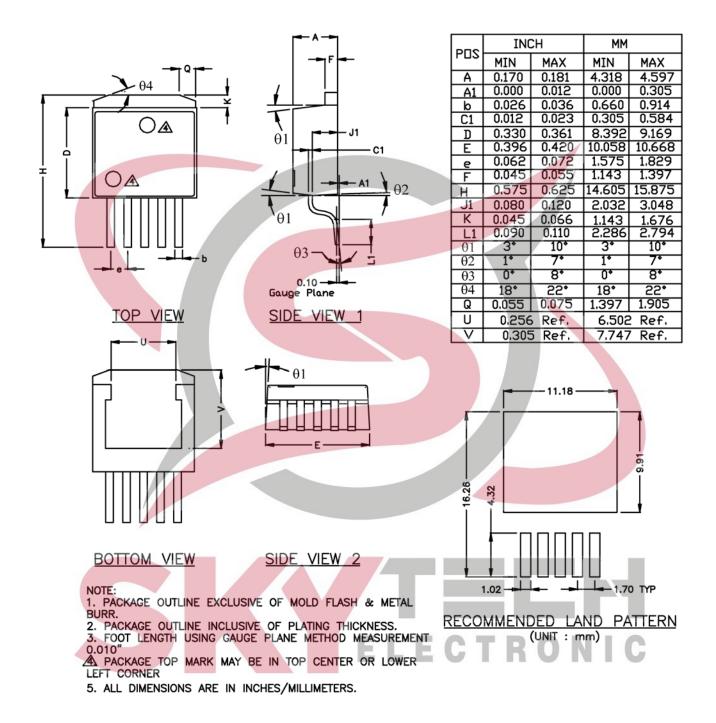




MIC29302A Evaluation Board Bottom Silk

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Package Information⁽¹⁶⁾



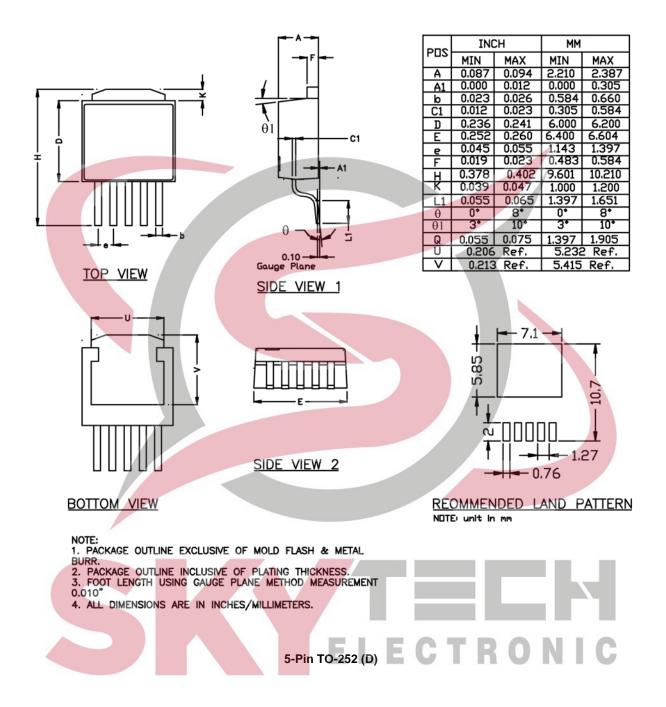
5-Pin TO-263 (U)

Note:

16. Package information is correct as of the publication date. For updates and most current information, go to <u>www.micrel.com</u>.

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Package Information (Continued)





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